FIG. 1

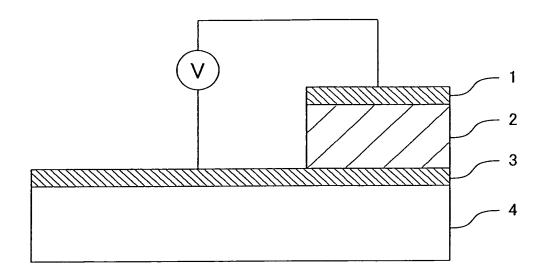
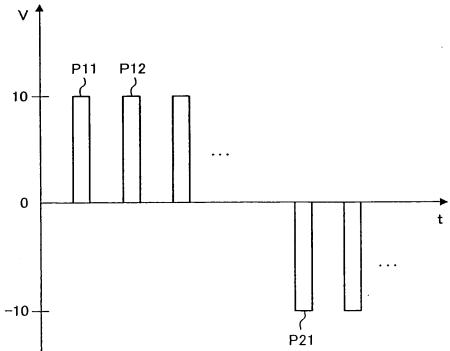


FIG. 2



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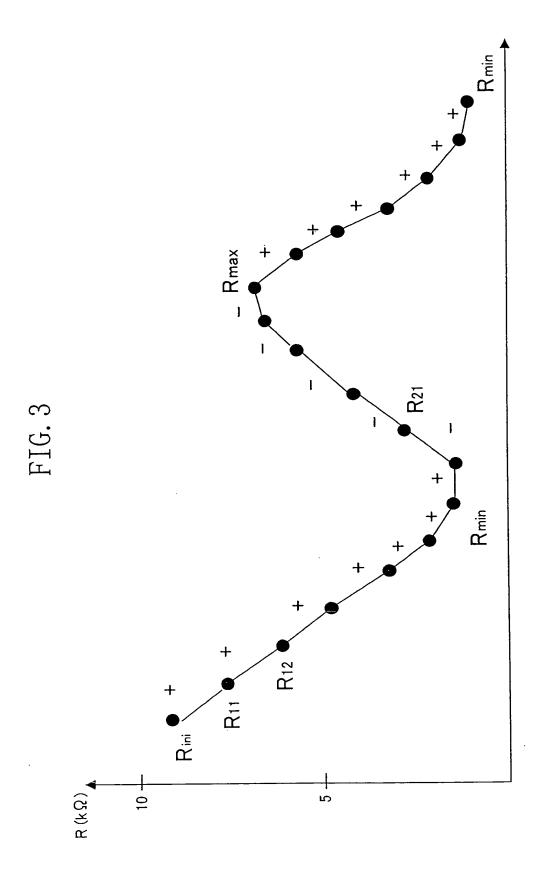
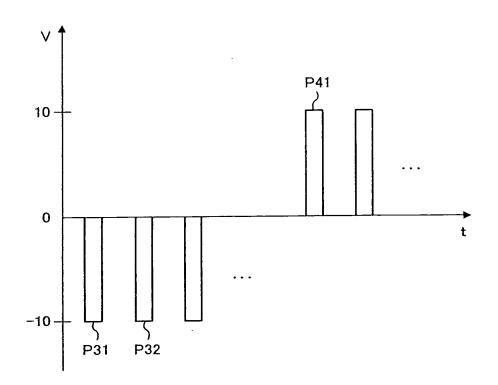
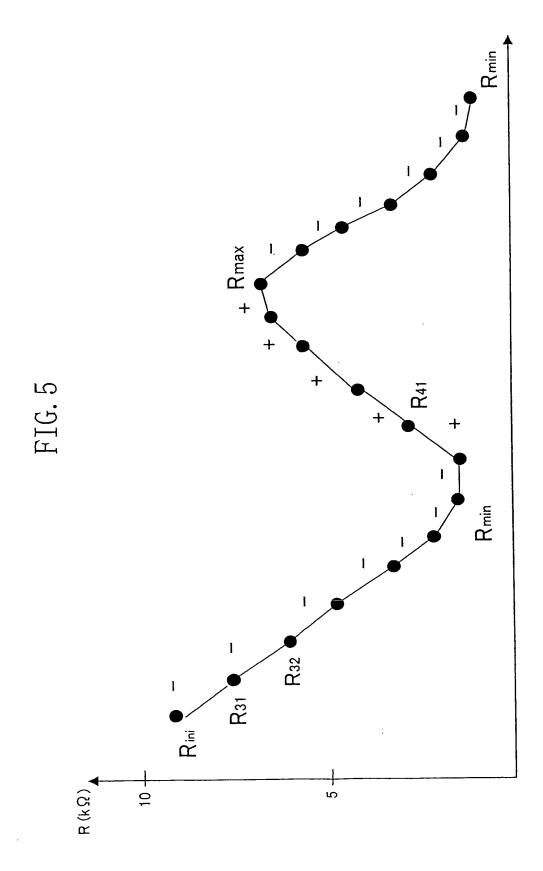


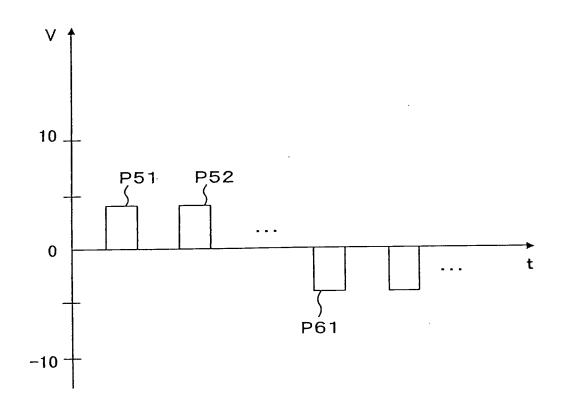
FIG. 4





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FIG. 6



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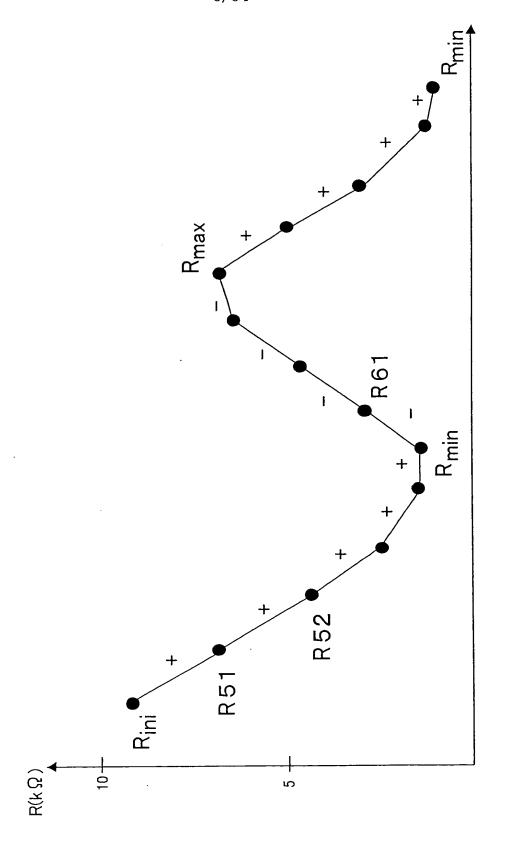
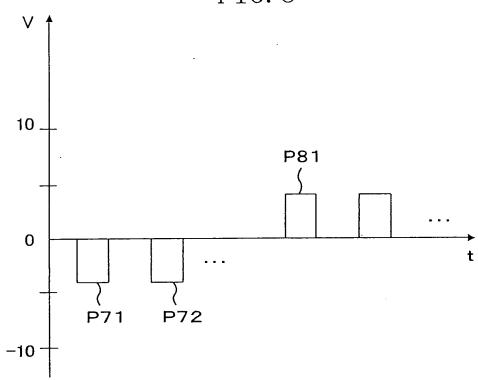
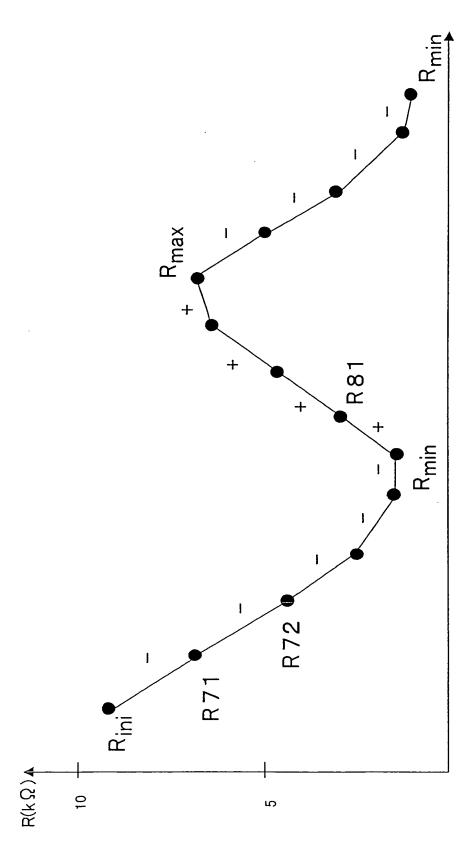


FIG. 7

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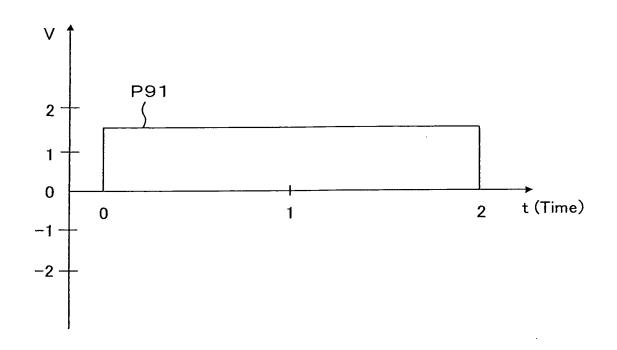


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FIG. 10



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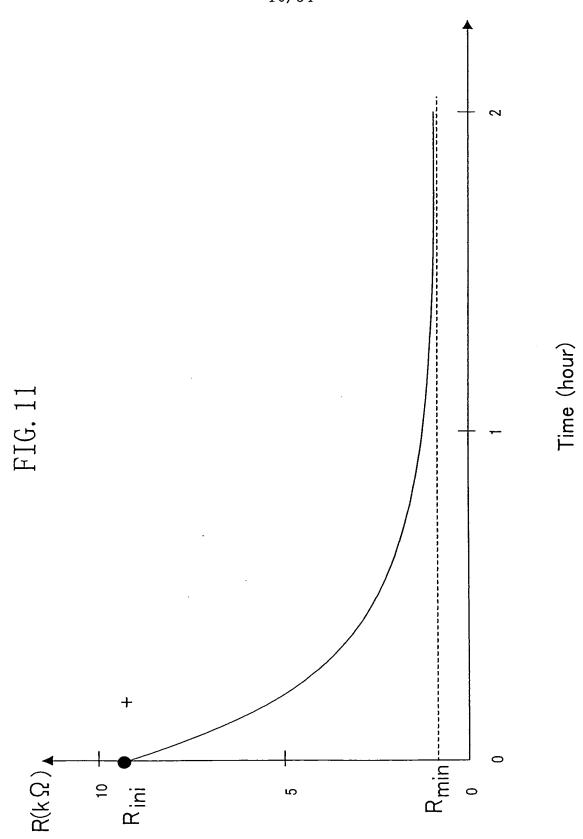
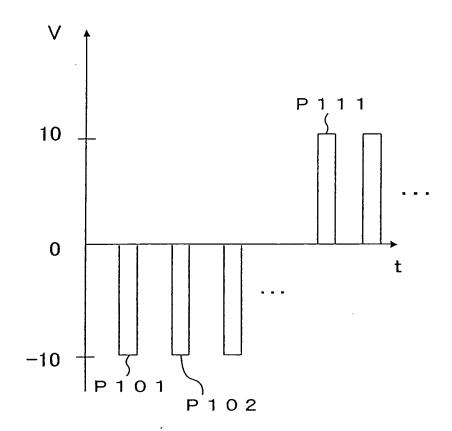


FIG. 12



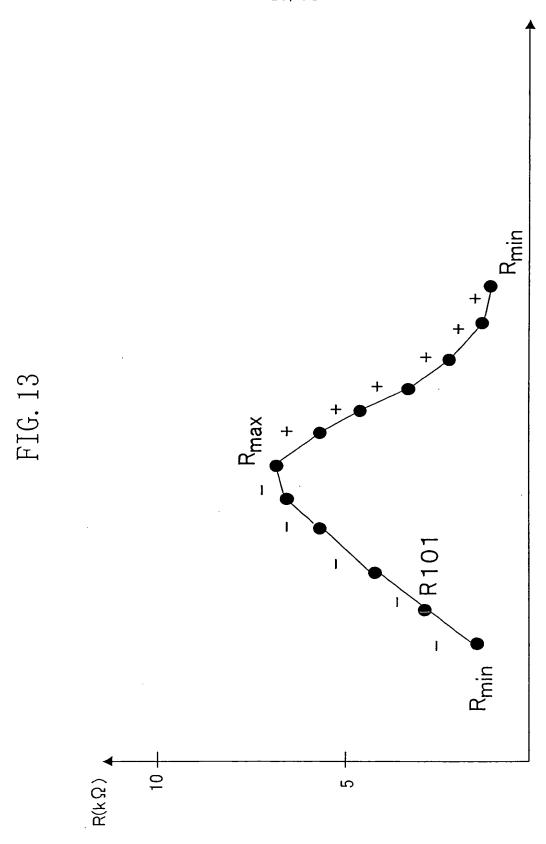
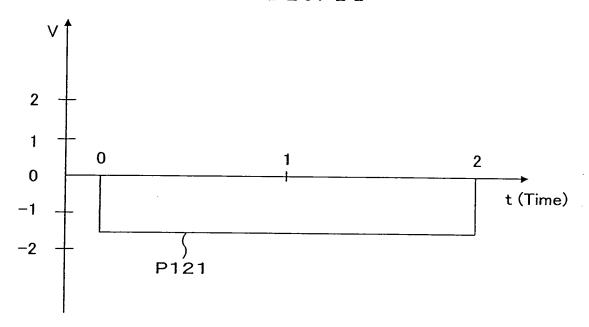


FIG. 14



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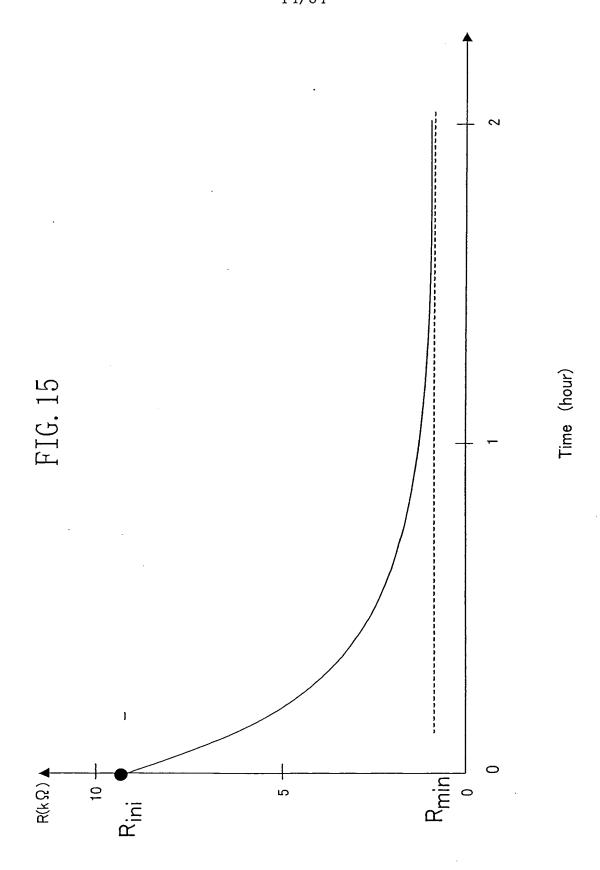
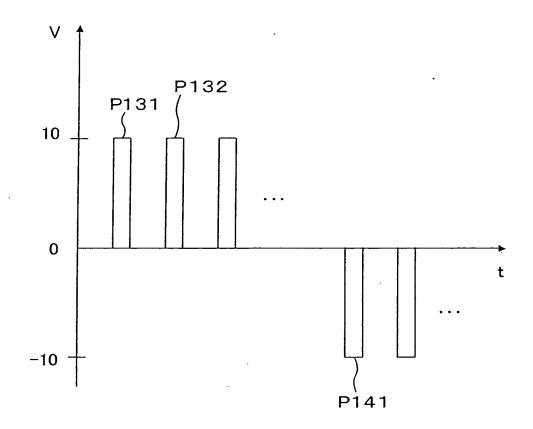


FIG. 16



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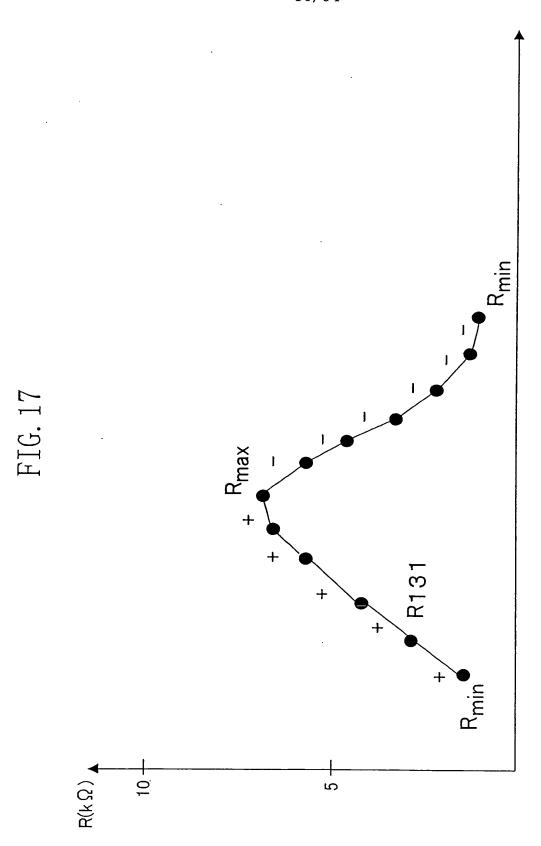
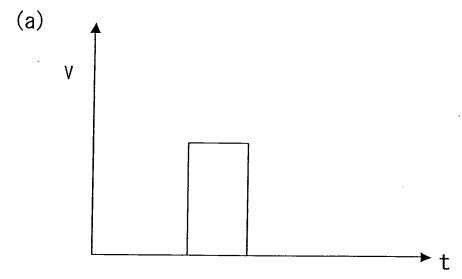
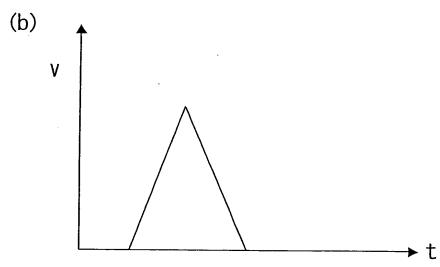


FIG. 18





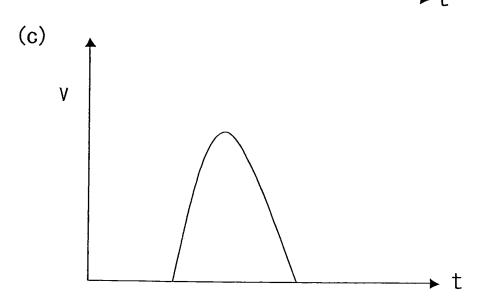


FIG. 19

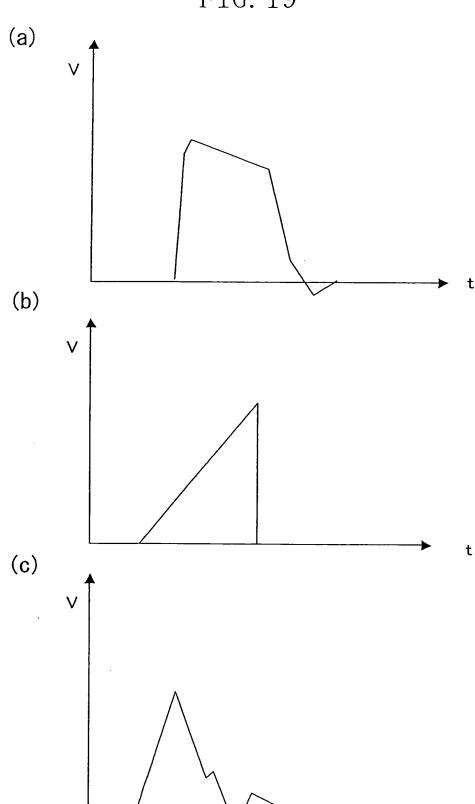


FIG. 20

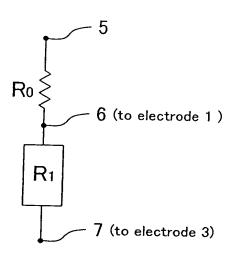


FIG. 21

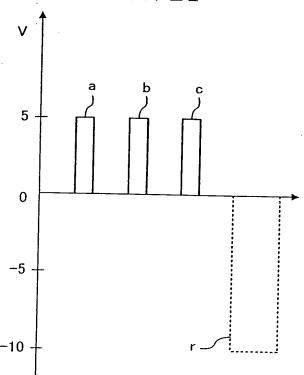
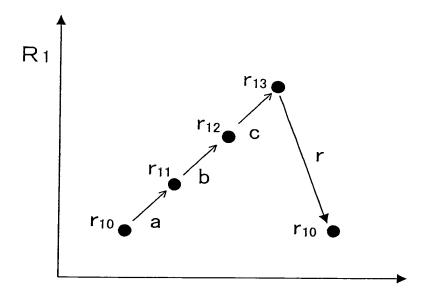


FIG. 22



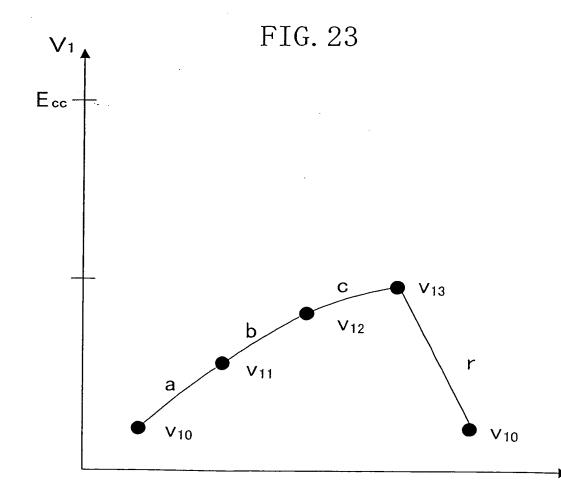


FIG. 24

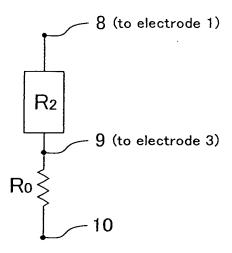
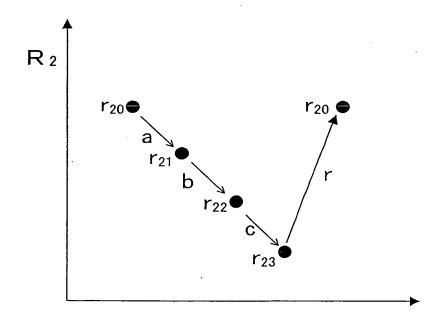
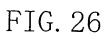


FIG. 25





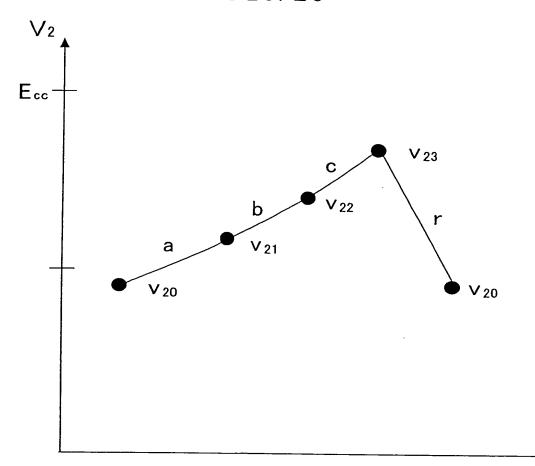


FIG. 27

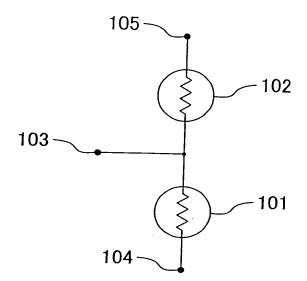
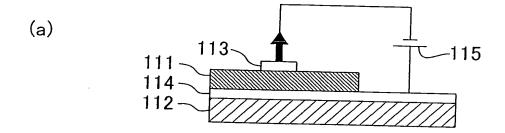


FIG. 28



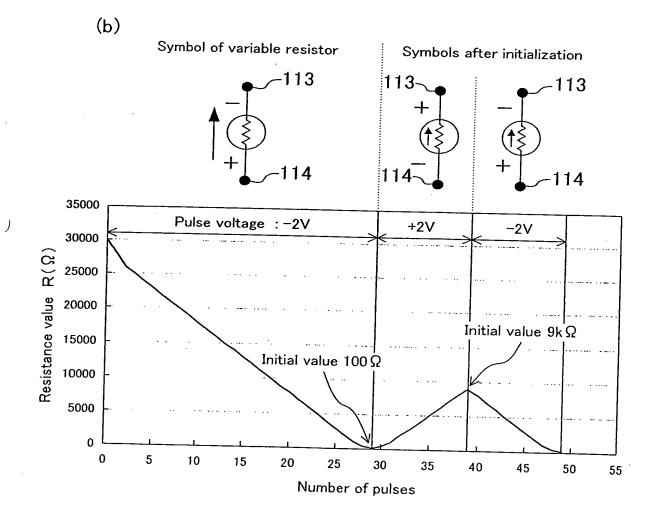
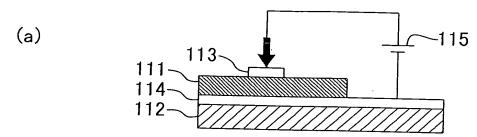


FIG. 29



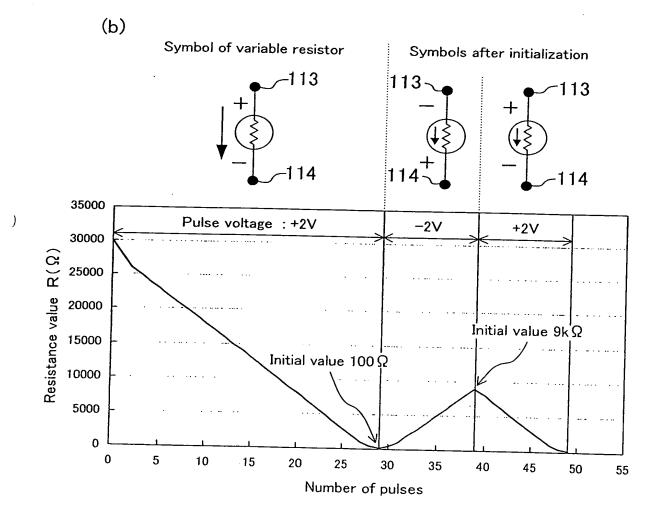
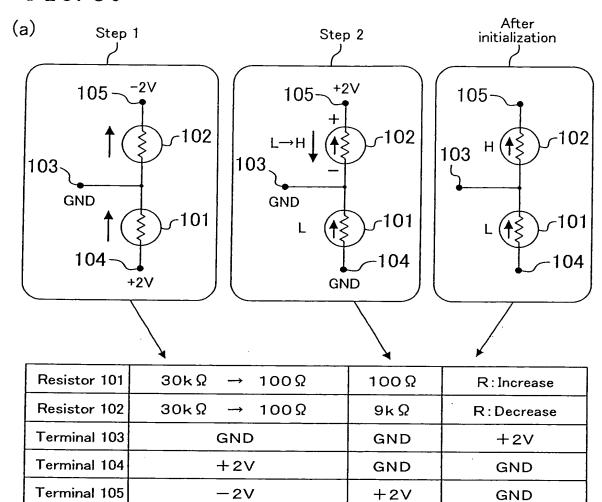


FIG. 30



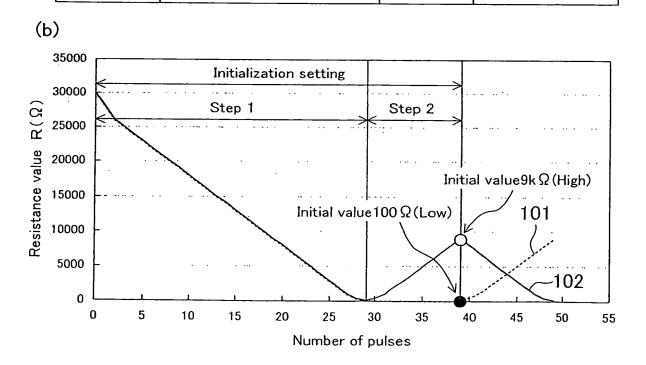
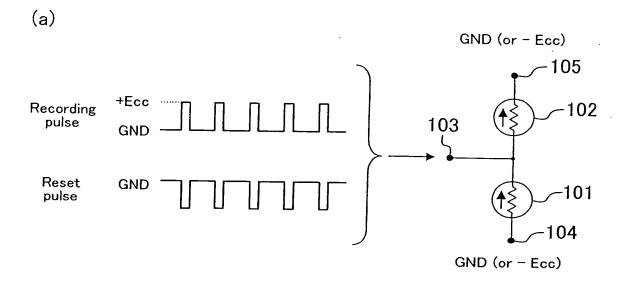


FIG. 31



(b)

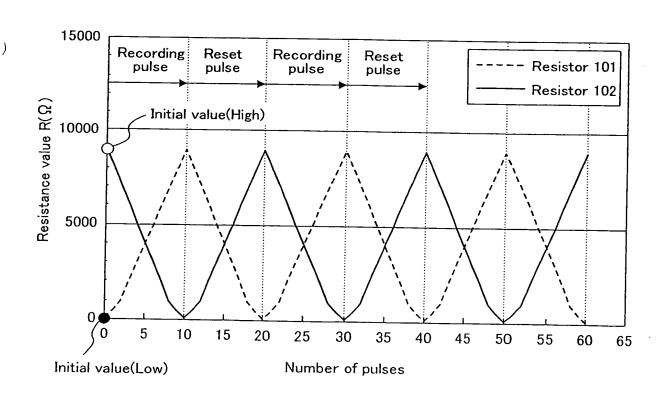


FIG. 32

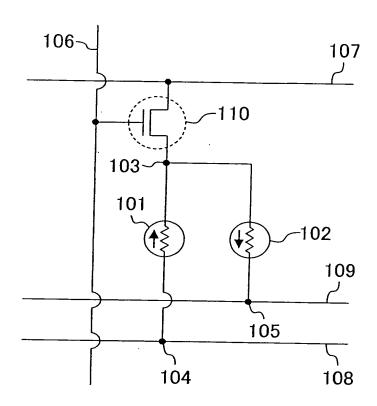
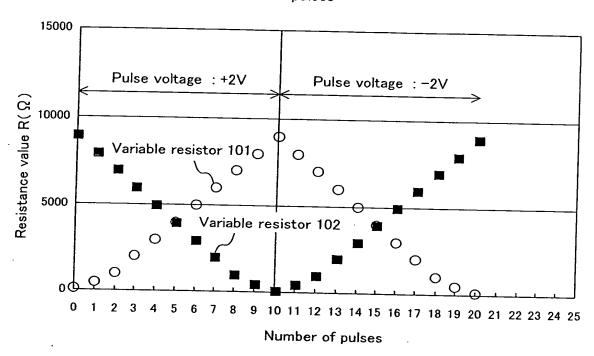


FIG. 33

(a) Resistance variation over number of pulses

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(b) Output voltage when reading recorded state

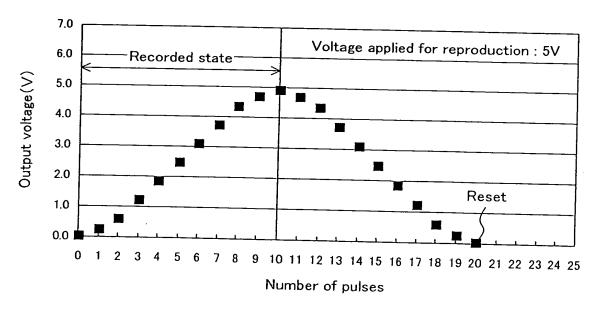
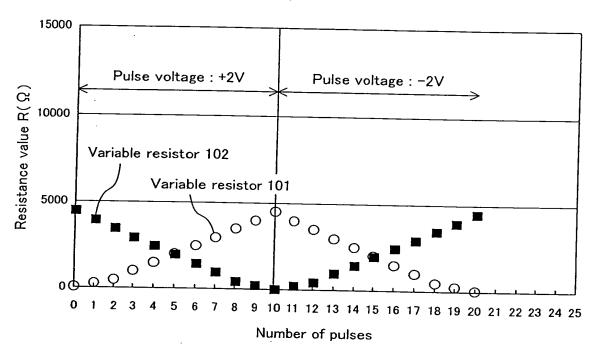


FIG. 34

(a) Resistance variation over number of pulses



(b) Output voltage when reading recorded state

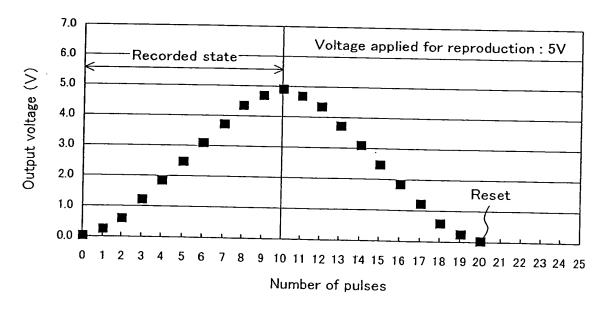
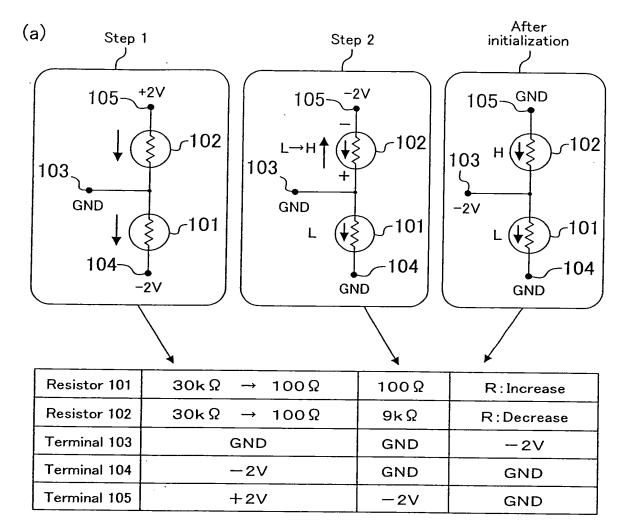


FIG. 35

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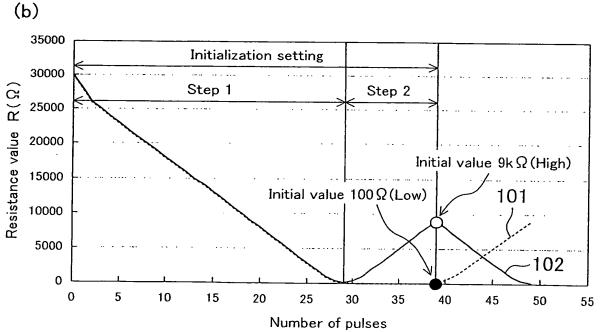
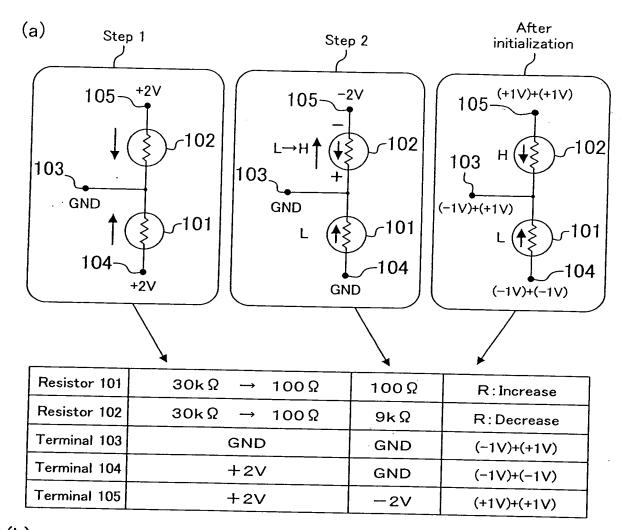


FIG. 36

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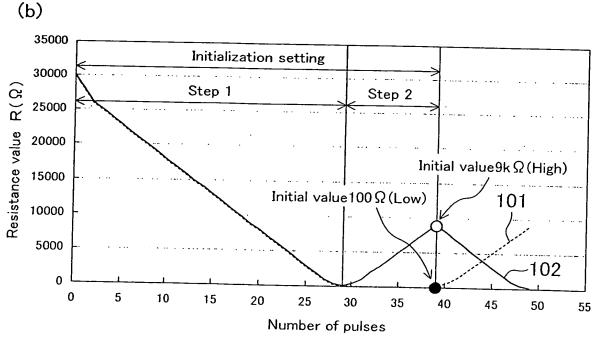


FIG. 37

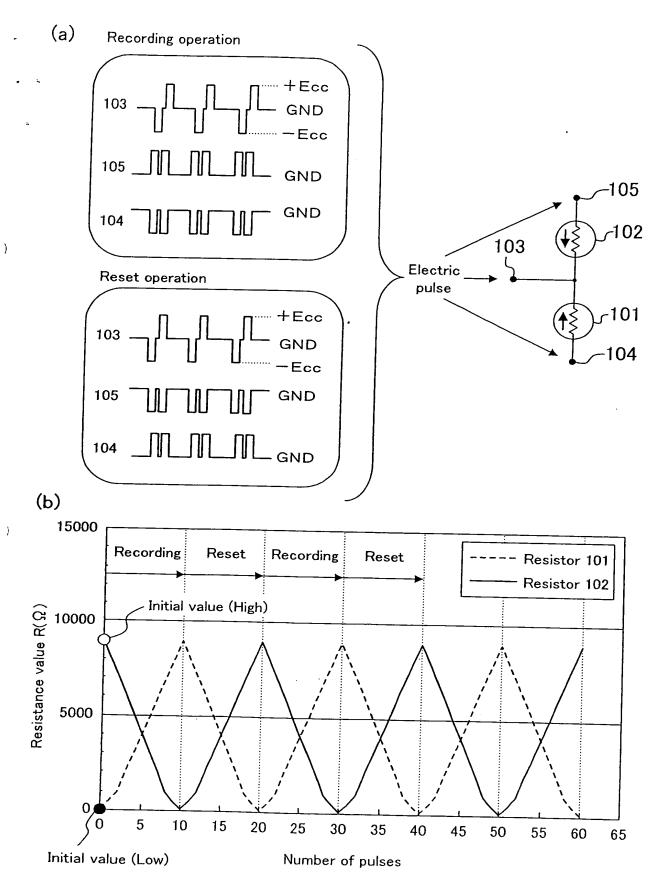


FIG. 38

